



15.32/5685

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of: ) Group Art Unit: 2814  
KOMIYAMA et al. )  
Serial No. 09/776,528 ) Examiner: Ha, Nathan W.  
Filed: February 4, 2001 )  
For: METHODS FOR MANUFACTURING )  
SEMICONDUCTOR CHIPS, METHODS FOR )  
MANUFACTURING SEMICONDUCTOR )  
DEVICES, SEMICONDUCTOR CHIPS, )  
SEMICONDUCTOR DEVICES, CONNECTION )  
SUBSTRATES AND ELECTRONIC DEVICES )

**PETITION FOR EXTENSION  
OF TIME**

Commissioner of Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

In accordance with Rule 1.136, the applicant respectfully petitions the Commissioner for a one month extension of time extending to Monday, August 25, 2003, the period for response to the Office Action dated April 24, 2003 (August 24 was a Sunday). The \$110 fee and the responsive papers are attached. If it should be determined that a longer extension of time is required to prevent this application from becoming abandoned, or for any other reason an insufficient fee has been paid, please charge any insufficiency to Deposit Account No. 50-0585. A duplicate copy of this petition is enclosed.

Respectfully submitted,

  
Alan S. Raynes

Reg. No. 39,809


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Dated: August 25, 2003

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**CERTIFICATE OF MAILING**

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on August 25, 2003.

  
Alan S. Raynes

August 25, 2003  
Date